

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: To be assigned

Filing Date: Herewith

Applicant: Hiroyasu Ito et al.

Group Art Unit: To be assigned

Examiner: To be assigned

Title: Semiconductor Device And Method Of Manufacturing  
The Same

Attorney Docket: 4041J-000437/DVA

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Director of The United States Patent and Trademark Office  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

**PRELIMINARY AMENDMENT**

Sir:

Preliminary to the calculation of the filing fee and the examination of this divisional application on its merits, please amend the application as follows.

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** begin on page 5 of this paper.

**Remarks** begin on page 18 of this paper.